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APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/817,216 03/27/2001		Yasuhiko Kuriyama	205263US2	7926
22850 7	590 03/26/2003			
•	•	ID, MAIER & NEUSTADT, P.C.	EXAMINER	
1940 DUKE S' ALEXANDRI			CHOE, HENRY	
			ART UNIT	PAPER NUMBER
			2817	
		DATE MAILED: 03/26/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary

Application No. 09/817,216

Applicant(s)

Kuriyama

Examiner

Henry Choe

Art Unit 2817

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The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the correspondence address					
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.						
	(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the					
. If the period for reply specified above is less than thirty (30) days, a reply y	apply and will expire SIX (6) MONTHS from the mailing date of this communication.					
Status						
1) X Responsive to communication(s) filed on Jun 2	28, 2002					
2a) X This action is FINAL . 2b) This	s action is non-final.					
3) Since this application is in condition for allowa closed in accordance with the practice under E	nce except for formal matters, prosecution as to the merits is Ex parte Quayle, 1935 C.D. 11; 453 O.G. 213.					
Disposition of Claims						
4) 💢 Claim(s) <u>1-18</u>	is/are pending in the application.					
4a) Of the above, claim(s)	is/are withdrawn from consideration.					
	is/are allowed.					
	is/are rejected.					
7)	is/are objected to.					
	are subject to restriction and/or election requirement.					
Application Papers						
9) \square The specification is objected to by the Examine	er.					
10) The drawing(s) filed oni	s/are a) \square accepted or b) \square objected to by the Examiner.					
Applicant may not request that any objection to	the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
	is: a) \square approved b) \square disapproved by the Examiner.					
If approved, corrected drawings are required in re						
12) The oath or declaration is objected to by the E	xaminer.					
Priority under 35 U.S.C. §§ 119 and 120						
a) Acknowledgement is made of a claim for foreign a) All b) Some* c) None of:	gn priority under 35 U.S.C. § 119(a)-(d) or (f).					
 X Certified copies of the priority documents Certified copies of the priority documents 						
	have been received in Application No					
application from the International I *See the attached detailed Office action for a list of	ty documents have been received in this National Stage Bureau (PCT Rule 17.2(a)). of the certified copies not received.					
14) Acknowledgement is made of a claim for dome						
a) The translation of the foreign language provis	i i i i i i i i i i i i i i i i i i i					
15) \square Acknowledgement is made of a claim for dome	stic priority under 35 U.S.C. §§ 120 and/or 121.					
Attachment(s)	_					
1) Notice of References Cited (PTO-892)	4) Interview Summary (PTO-413) Paper No(s).					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s).	5) Notice of Informal Patent Application (PTO-152)					
omation Disclosure Statement(s) (F10-1449) Paper No(s)	6) Other:					

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1 and 9 are still rejected under 35 U.S.C. 102(b) as being anticipated by Maeda et al (Fig. 27).

Regarding claim 1, Maeda et al (Fig. 27) discloses an amplifier circuit comprising a first capacitor element (2735) which is connected between the signal input terminal (2701) and an input terminal (2702) of the first transistor (2240), a second capacitor element (2736) which is connected between the signal input terminal (2701) and an input terminal (2703) of the second transistor (2244), and a first impedance element (2720) one end (upper end of 2720) of which is connected to the input terminal (2702) of the first transistor (2240) and the other end (bottom end) of which is connected to the input terminal (2703) of the second transistor (2244).

Regarding claim 9, Maeda et al (Fig. 27) also discloses a power amplifier circuit comprising a bias circuit (2731, 2732, 2733, 2734) for supplying a direct current bias voltage to the respective input terminals of the first and second transistors [a bias circuit (2731 and 2732) supplies a direct current bias voltage to the input terminal (2702) of the first transistor (2240) and

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a bias circuit (2733 and 2734) supplies a direct current bias voltage to the input terminal (2703) of the second transistor (2244); see column 23, lines 43-46].

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claim 11 is rejected under 35 U.S.C. 103(a) as being unpatentable over Maeda et al (Fig. 27).

Maeda et al (Fig. 27) further discloses a power amplifier circuit comprising a plurality of impedance elements (2732 and 2734) which are connected between the respective input terminals (2702 and 2703) of the first (2240) and second (2244) transistors and bias terminals (2731 and 2733). This arrangement is functionally equivalent to the claimed impedance elements connected between the transistor input terminals and a bias output terminal.

Response to Arguments

5. Applicant's arguments with respect to claims 1, 9 and 11 have been considered but are moot in view of the new ground(s) of rejection.

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Allowable Subject Matter

6. Claims 2-8, 10 and 12-18 are allowed.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office 7.

action. Accordingly, THIS ACTION IS MADE FINAL. See MPEP § 706.07(a). Applicant is

reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE

MONTHS from the mailing date of this action. In the event a first reply is filed within TWO

MONTHS of the mailing date of this final action and the advisory action is not mailed until after

the end of the THREE-MONTH shortened statutory period, then the shortened statutory period

will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR

1.136(a) will be calculated from the mailing date of the advisory action. In no event, however,

will the statutory period for reply expire later than SIX MONTHS from the date of this final

action.

Name: Henry Choe

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